Title: Coherency Techniques for Suspending Execution of a Thread

Until a Specified Memory Access Occurs 1st Named Inventor: David L. Hill

Application No.: 10/039,656 Docket No.: 42390P13635

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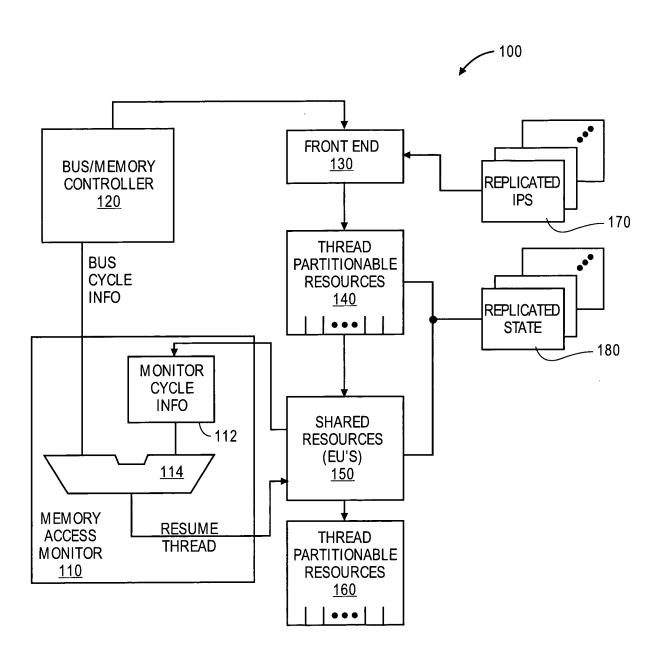


FIG. 1

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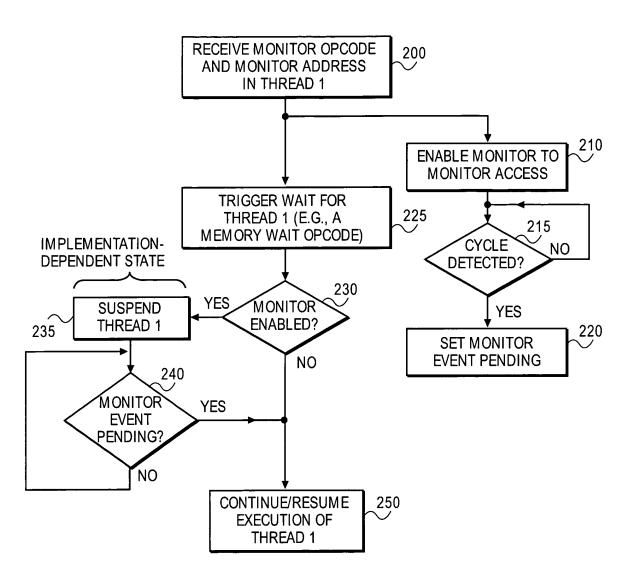


FIG. 2

Title: Coherency Techniques for Suspending Execution of a Thread Until a Specified Memory Access Occurs

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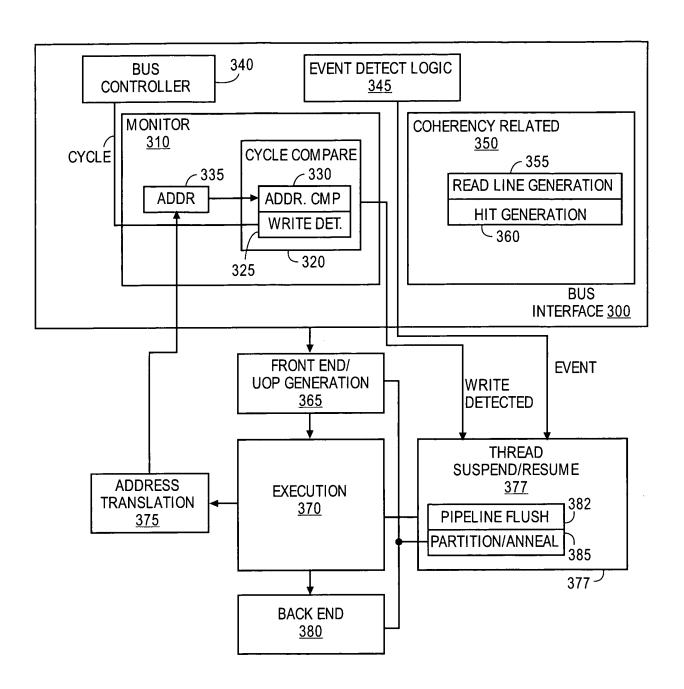


FIG. 3

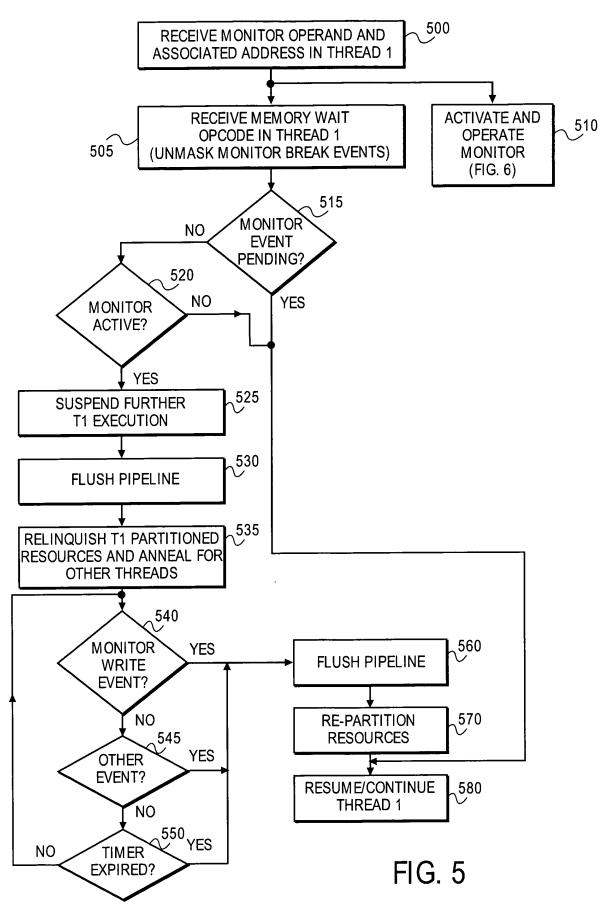
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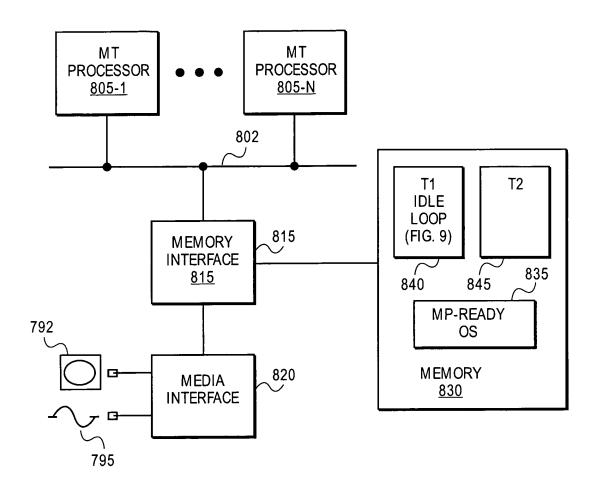
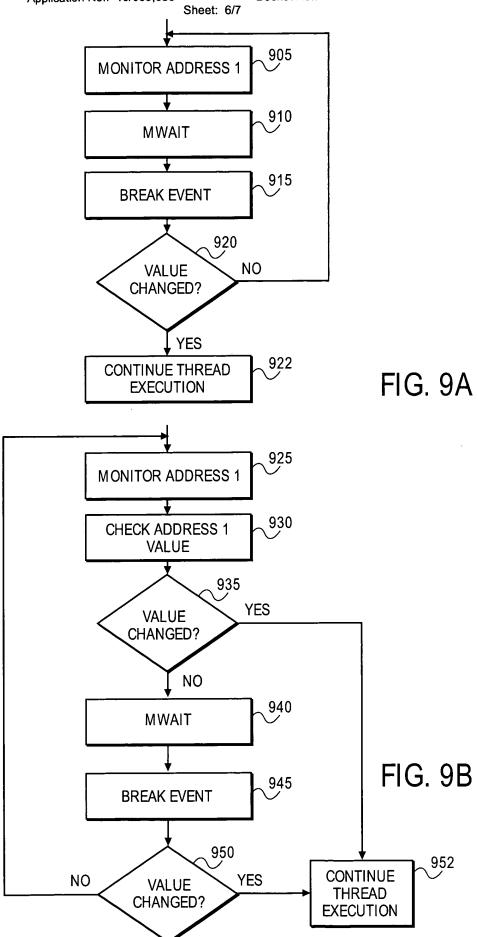


FIG. 8

Title: Coherency Techniques for Suspending Execution of a Thread Until a Specified Memory Access Occurs

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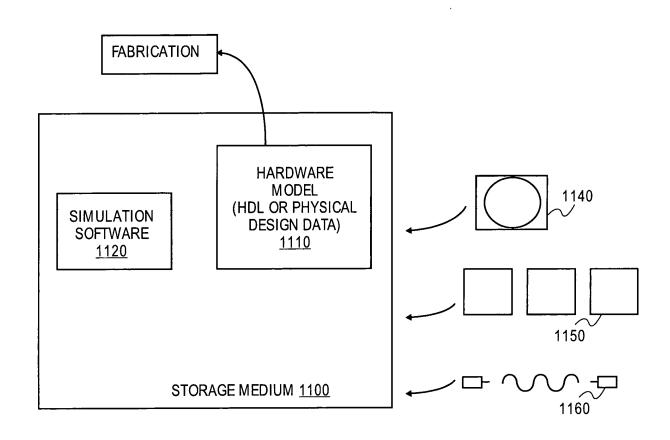


FIG. 11